

## CLAIMS

1. A voltage boosting circuit comprising:

a plurality of boosting capacitors, including a first boosting capacitor connected to a driving node and a last boosting capacitor configured to output a boosting voltage; and  
a plurality of switches configured to connect the plurality of boosting capacitors in series in response to a control signal,

wherein a boosting voltage level is configured to vary as the voltage level at the driving node changes, according to a logic state of a boosting level control signal.

2. The voltage boosting circuit of claim 1, configured such that when the boosting level control signal is in a first logic state, the voltage level at the driving node changes from a ground voltage level to an external supply voltage level, thus increasing the boosting voltage level.

3. The voltage boosting circuit of claim 1, configured such that when the boosting level control signal is in a second logic state, the voltage level at the driving node is fixed at a ground voltage level, thus decreasing the boosting voltage level.

4. The voltage boosting circuit of claim 1, wherein the logic state of the boosting level control signal is configured to enter a logic high state or a logic low state in response to an external supply voltage level.

5. The voltage boosting circuit of claim 4, further comprising an external supply voltage detector configured to detect the external supply voltage level and to generate the boosting level control signal.

6. A method of generating a boosting voltage in a voltage boosting circuit that includes a plurality of boosting capacitors with a first boosting capacitor connected to a driving node and a last boosting capacitor that outputs the boosting voltage, and a plurality of switches that connects the plurality of boosting capacitors in series in response to a control signal, the method comprising:

increasing the boosting voltage by changing a voltage level at the driving node from a ground voltage level to an external supply voltage level when a boosting level control signal is in a first logic state; and

5 decreasing the boosting voltage by fixing the voltage level at the driving node to the ground voltage level when the boosting level control signal is in a second logic state.

7. The method of claim 6, further comprising:

10 detecting the external supply voltage level and changing the boosting level control signal to the first logic state when the external supply voltage level is less than a reference voltage level; and

detecting the external supply voltage level and changing the boosting level control signal to the second logic state when the external supply voltage level is greater than the reference voltage level.